



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN THE APPLICATION OF:

**WILLIAM J. BORLAND ET AL.**

CASE NO.: **EL0499 US NA**

APPLICATION NO.: **10/664,638**

CONFIRMATION NO.: **5044**

GROUP ART UNIT: **2836**

EXAMINER:

FILED: **SEPTEMBER 18, 2003**

FOR: **HIGH TOLERANCE EMBEDDED CAPACITORS**

**SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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In compliance with 37 CFR 1.97 and 1.98, Applicant would like to bring to the attention of the U.S. Patent and Trademark Office information that may be helpful in the examination of the above-identified patent application. All of the information is listed on an attached Form PTO/SB/08.

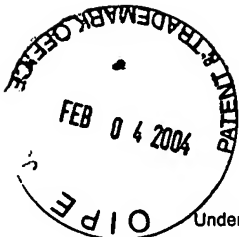
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Respectfully submitted,

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Dated: 1/30/2004

Enclosures



PTO/SB/08b (08-03)

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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/664,638
Filing Date	September 18, 2003
First Named Inventor	William J. Borland
Art Unit	2836
Examiner Name	
Attorney Docket Number	EL0499 US NA

Sheet

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of

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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		DR. KIM FJELDSTED AND STACEY L. CHASE, EMBEDDED PASSIVES: LASER TRIMMED RESISTORS, CIRCUITREE, Volume 70, March 2002.	<input type="checkbox"/>
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10/664,638

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FORM PTO SB08

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